

Substitute for form 1449/PTO				Complete if Known	
FIRST SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>					
Sheet 1 of 2					
Application Number		10/694,729			
Filing Date		October 29, 2003			
First Named Inventor		Hoang T. TRAN			
Art Unit		2182			
Examiner Name		Sun, Scott C.			
Attorney Docket Number		1875.4520000			

U.S. PATENT DOCUMENTS					
Examiner Initials ¹	Cite No. ¹	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)	MM-DD-YYYY		
SS	US1	4,627,070	12-02-1986	Champlin et al.	
	US2	4,833,695	05-23-1989	Greub	
	US3	4,967,201	10-30-1990	Rich, III	
	US4	5,726,991	03-10-1998	Chen et al.	
	US5	5,793,990	08-11-1998	Jirgal et al.	
	US6	5,933,021	08-03-1999	Mohd	
	US7	6,081,570	06-27-2000	Ghuman et al.	
	US8	6,140,956	10-31-2000	Hillman et al.	
	US9	6,259,693	07-10-2001	Ganmukhi et al.	
	US10	6,137,734	12-24-2000	Schoner et al.	
	US11	6,317,804 B1	11-13-2001	Levy et al.	
	US12	6,438,717 B1	08-20-2002	Butler et al.	
	US13	6,483,849 B1	11-19-2002	Bray et al.	
	US14	7,032,139 B1	04-18-2006	Iryarni et al.	
	US15	7,035,228 B2	04-25-2006	Baumer	
	US16	2001/0012288 A1	08-09-2001	Yu	
	US17	2002/0054569 A1	05-09-2002	Morkawa	
	US18	2003/0009307 A1	01-09-2003	Mejia et al.	
	US19	2003/0048781 A1	03-13-2003	Peirson	
	US20	2004/0083077 A1	04-29-2004	Baumer et al.	

FOREIGN PATENT DOCUMENTS					
Examiner Initials ¹	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)	MM-DD-YYYY		T ⁶
	FP1				
	FP2				
	FP3				
	FP4				
	FP5				
	FP6				
	FP7				
	FP8				
	FP9				

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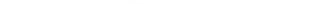
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		Number-Kind Code ² (If Known)			
X	US21	2004/0141497 A1	07-22-2004	Amirichirneh et al.	
	US22	2004/0141531 A1	07-22-2004	Amirichirneh et al.	
	US23	2004/0117698 A1	06-17-2004	Tran et al.	
	US24	2005/0190690 A1	09-01-2005	Tran	
	US25	2006/0250985 A1	11-09-2006	Baumer	
	US26				
	US27				
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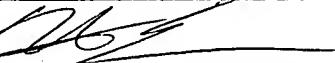
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
	NPL1	Copy of International Search Report Issued Dec. 14, 2004 for International Application No. PCT/US03/34234, 5 pages.			
	NPL2	Alaska Quad-Port Gigabit Ethernet Over Copper Transceivers, from http://www.marvell.com/products/transceivers/quadport/ ..., 1 page, printed May 8, 2007.			
	NPL3	Alaska X10 Gigabit Ethernet Alaska Gigabit Ethernet Fast Ethernet Physical Layer (PHY) Transceiver Families Provide a Full Range of Ethernet Transceiver Solutions for the Broadband Communications Industry, from http://www.nuhorizons.com/FeaturedProducts/Volume3/Marvell/phy_transceiver.asp , 4 pages, Copyright 2007, printed May 8, 2007.			
	NPL4	Cisco Search: Results for "cdl" within "News@Cisco", from http://www.cisco.com/cgi-bin/search/search.pl , Copyright 1992-2003, printed July 29, 2003.			
	NPL5	Industry Breakthrough: Marvell Announces the First Quad-Port Transceiver to Support Both Copper and Fiber-Optic Gigabit Ethernet Interfaces, from Business Wire at www.encyclopedia.com/printable/aspx?id=1G1:68912211 , 4 pages, January 9, 2001, printed May 8, 2007.			
	NPL6	David Maliniak (ed.), Bel's integrated connector modules support Marvell's Alaska quad Gigabit Ethernet transceiver, from http://www.electronicsweb.com/Content/news/ ..., 1 page, December 14, 2000, printed May 8, 2007.			
	NPL7	Marvell Gets Small, from http://www.lightreading.com/document.asp?doc_id=12004&print=true , 1 page, February 19, 2002, printed May 8, 2007.			
	NPL8	Marvell Introduces the Industry's Smallest Quad-Port Gigabit Transceiver Device, Enabling Ultra High Port Density Enterprise Switching Systems, from http://www.marvell.com/press/pressNewsDisplay.do?releaseID=41 , 3 pages, February 19, 2002, printed May 8, 2007.			
	NPL9	Ed Turner, David Law, Sep. 17-19, 2001, "IEEE P802.3ae MDC/MDIO", http://www.ieee802.org/3/efm/public/sep01/turner.sub.--1.sub.--0901.pdf (copy attached).			
	NPL10	Q&A: Hiroshi Suzuki on Extending Ethernet Beyond the LAN, from http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_qa.html , 3 pages, Copyright 1992-2003, printed July 29, 2003.			

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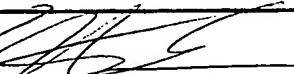
¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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	NPL11	IEEE Std. 802.3, Part 3: Carrier Sense Multiple Access with Collision Detection (CSMA/CD) access method and physical layer specifications, Section Two, pp. 9-55, Copyright 2002.	
	NPL12	IEEE Std. 802.3, Part 3: Carrier Sense Multiple Access with Collision Detection (CSMA/CD) access method and physical layer specifications, Section Four, pp. 9-152, Copyright 2005.	
	NPL13	Cisco Systems, Hiroshi Suzuki: Taking Ethernet Beyond the LAN, http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_profile.html	
	NPL14	Cisco Systems, Q&A: Hiroshi Suzuki on Extending Ethernet Beyond the LAN, http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_qa.html	
	NPL15	Cisco Systems, Innovators Profiles, http://newsroom.cisco.com/dlls/innovators/inn_profiles.html	
	NPL16	Cisco Systems, Optical, http://newsroom.cisco.com/dlls/innovatros/optical/	
	NPL17	Cisco Systems, Cisco Systems ad Areteae Interactive Launch Internet Powered Property (IP2), http://newroom.cisco.com/dlls/global/asiapac/news/2000/pr_07-25.html .	
	NPL18		
	NPL19		
	NPL20		

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